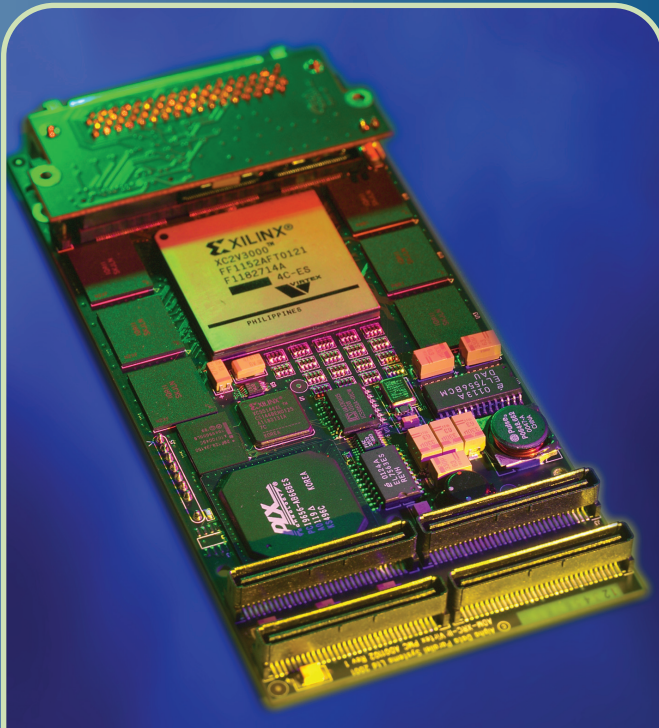
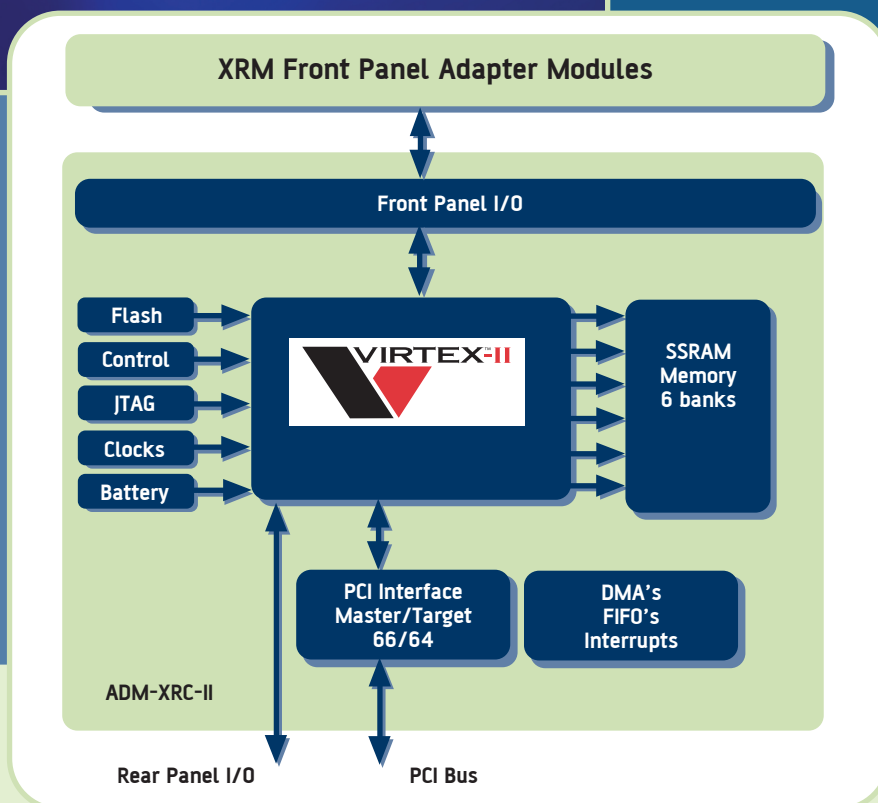


XILINX RECONFIGURABLE COMPUTER



Features

- Industry standard Xilinx Virtex-II based PMC ideal for DSP and Reconfigurable Computing
- Support for the largest Xilinx devices from 2V1000 to 2V8000
- Adapters available for PCI, CompactPCI and VME
- Flexible configuration of independent banks of ZBT SRAM, DDR SRAM, DDR SDRAM and Flash memory
- PCI bandwidth up to 533Mbytes/sec. High performance bus mastering PCI interface with twin DMA
- Flexible front and rear panel I/O interface options, bandwidth up to 5GBytes/sec
- Programmable clock generators
- Support for Handel-C, PAVE, System Generator and ChipScope
- Drivers for WinNT, Win2000, WinXP, VxWorks and Linux
- Comprehensive SDK with template designs and common API for easy cross platform development



**XILINX VIRTEX-II RECONFIGURABLE COMPUTER**
**Specifications**

		<b>ADM-XRC-II</b>
<b>Virtex-II FPGA</b>		2V3000, 2V4000, 2V6000, 2V8000
<b>PCI Bus</b>		66MHz 64-bit PCI
<b>FPGA Local Bus</b>		32-bit 66MHz
<b>Memory</b>	<b>SRAM</b>	6 independent ZBT SRAM banks 256K/512K/1024K x 36 additional 2 banks via XRM-ZBT
	<b>DRAM</b>	256MByte via XRM-DDR
	<b>Flash</b>	16 MByte
<b>I/O</b>		XRM Bus, Pn4 connector
<b>Configuration</b>		SelectMap, Flash, JTAG header
<b>Software Development Kit</b>	<b>Windows</b>	WinNT, XP, 2000
	<b>Linux</b>	Red Hat, Hard Hat
	<b>VxWorks</b>	

**XRM-Adapters**

XRM-I0146	High Density LVDS/LVTTL
XRM-I034	Low Density LVDS/LVTTL
XRM-DDR	1 bank DDR SDRAM, LVDS/LVTTL
XRM-ZBT	2 banks ZBT SSRAM, RS-232
XRM-FPDP	Front Panel Data Port
XRM-ADC	1/2 ch 105MSPS 14-bit ADC
XRM-DAC	2 ch 105MHz 14-bit DAC
XRM-ETH	Ethernet, LDVS/LVTTL, RS-232
XRM-RIO	RapidIO
XRM-CLINK	CameraLink

**Ordering Information**
**ADM-XRC-II/xxx-y/z/(xrm)**

xxx	Virtex-II size	3000, 4000, 6000 or 8000
y	Virtex-II speed	4, 5 or 6
z	ZBT SSRAM	6, 12 or 24 MByte
xrm	XRM front panel adapter	